

# EI E 482: Digital CMOS VLSI Design

Design, layout, simulation, and test of design custom digital CMOS/VLSI chips, using a CMOS cell library and state-of-the-art CAD tools. Digital CMOS static and dynamic gates, flip flops, CMOS array structures commonly used in digital systems.

3 Credits

### Prerequisites

- EI E 385: Advanced Digital Systems \$target.descriptions.MinimumGrade\$
- Pre-Requisite: 24 Earned Hours

## Instruction Type(s)

Lecture: Lecture for EI E 482

### Course Fee(s)

- Electrical Eng
- \$50.00

## **Subject Areas**

- <u>Computer Hardware Engineering</u>
- Electrical and Electronics Engineering

The University of Mississippi is accredited by the Southern Association of Colleges and Schools Commission on Colleges to award certificates and baccalaureate, master's, specialist, and doctoral degrees. Contact the Commission on Colleges at 1866 Southern Lane, Decatur, Georgia 30033-4097, call 404-679-4500, or visit online at www.sacscoc.org for questions about the accreditation.

