

EI E 482: Digital CMOS VLSI Design

Electrical Engineering

Design, layout, simulation, and test of design custom digital CMOS/VLSI chips, using a CMOS cell library and state-of-the-art CAD tools. Digital CMOS static and dynamic gates, flip flops, CMOS array structures commonly used in digital systems.

3 Credits

Prerequisites

- [EI E 385: Advanced Digital Systems](#) \$target.descriptions.MinimumGrade\$
- Pre-Requisite: 24 Earned Hours

Instruction Type(s)

- Lecture: Lecture for EI E 482

Course Fee(s)

Electrical Eng

- \$50.00

Subject Areas

- [Computer Hardware Engineering](#)
- [Electrical and Electronics Engineering](#)

